



Information Disclosure Statement by Applicant (Use as many sheets as necessary) Sheet 1 of 1	Application No.	10/008,967
	Filing Date	November 9, 2001
	First Named Inventor	Robert Bruce Gage
	Group Art Unit	2116
	Examiner Name	A. I. Elamin
	Attorney's Docket No.	1466-US

U.S. Patent Documents					
Examiner Initials	Cite No.	Document Number Number-Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AIE	A1	US-5,153,883	10-06-1992	Hayashi et al.	
	A2	US-5,710,517	01-20-1998	Meyer	
	A3	US-5,712,883	01-27-1998	Miller et al.	
	A4	US-5,917,834	06-29-1999	Arkin	
	A5	US-5,935,256	08-10-1999	Lesmeister	
	A6	US-6,028,439	02-22-2000	Arkin et al.	
	A7	US-6,121,816	09-19-2000	Tonks et al.	
	A8	US-6,130,552	10-10-2000	Jefferson et al.	
	A9	US-6,188,253 B1	02-13-2001	Gage et al.	
	A10	US-6,208,702 B1	03-27-2001	Ghoshal	
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Foreign Patent Documents or Published Foreign Patent Applications							
Examiner Initials	Cite No.	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Translation	
						Yes	No
	B1						
	B2						
	B3						
	B4						

Non Patent Literature Documents (include author, title, date, pages, publisher and place of publication)		
Examiner Initials	Cite No.	Document
AIE	C1	L. Ashby, ASIC CLOCK DISTRIBUTION USING A PHASE LOCKED LOOP (PLL), Sept. 1991, pp. P1-6.1-6.3, IEEE, New York, NY.
	C2	

Examiner Signature	Date Considered 7/6/05
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Substitute Disclosure Form (PTO-1449)	